**UNIVERSITY OF CALIFORNIA, DAVIS**

**Department of Electrical and Computer Engineering**

**EEC 172 Spring 2023**

**LAB 1 Verification**

**Team Member 1: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

**Team Member 2: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

**Section Number/TA: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

**Demonstrate your working application to your TA**

|  |  |  |
| --- | --- | --- |
| Task 1 | | |
| Date | TA Signature | Notes |
|  |  |  |
| Task 2 | | |
| Date | TA Signature | Notes |
|  |  |  |
| Task 3 | | |
| Date | TA Signature | Notes |
|  |  |  |